

FILED: 4/24/2006

Sheet 1 of 1

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-619001	Application No. 10/024,502
Information Disclosure Statement by Applicant (Use several sheets if necessary)  (37 CFR §1.98(b))		Applicant Mark B. Rosenbluth et al.	
		Filing Date December 17, 2001	Group Art Unit 2186 2185

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SE	AA	5,649,092	07/1997	Price et al.	714	15	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature <i>S. Elman</i>	Date Considered 5/11/2006
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Disclosure Form (PTO-1449)

Substitute Form PTO-1449

U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
10559-619001Application No.  
10/024,502**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

Applicant

Mark B. Rosenbluth et al.

Filing Date

December 17, 2001

Group Art Unit

~~2186~~ 2185**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SE	AA	6,779,084	08/2004	Wolrich et al.	711	118	
SE	AB	6,426,957	07/2002	Hauser et al.	370	413	
SE	AC	2004/0179533	09/2004	Donovan	370	395.5	
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

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	AL							
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	AN							
	AO							
	AP							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
SE	AQ	Adiletta, et al., "The next generation of Intel IXP Network Processors", Intel Technology Journal, Network Processors, vol. 6, issue 3, published Aug. 15, 2002, pp. 6-18.
SE	AR	Kornaros, et al., "A fully-programmable memory management system optimizing queue handling at multi Gigabit rates", ACM, Jun. 2-6, 2003, pp. 54-59.
	AS	
	AT	

Examiner Signature

S.E./more

Date Considered

5/11/2006

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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SE	AA	2001/0014100	08/2001	Abe et al.	370	412	
SE	AB	2002/0131443	09/2002	Robinet et al.	370	442	
SE	AC	2002/0144006	10/2002	Cranston et al.	709	312	
SE	AD	2002/0196778	12/2002	Colmant et al.	370	352	
SE	AE	2003/0041216	02/2003	Rosenbluth et al.	711	141	
SE	AF	2003/0046488	03/2003	Rosenbluth et al.	711	108	
SE	AG	2003/0115426	06/2003	Rosenbluth et al.	711	154	
SE	AH	2004/0039895	02/2004	Wolrich et al.	712	200	
SE	AI	2004/0054880	03/2004	Bernstein et al.	712	245	
SE	AJ	2004/0071152	04/2004	Wolrich et al.	370	420	
SE	AK	2004/0073778	04/2004	Adiletta et al.	712	220	
SE	AL	2004/0098496	05/2004	Wolrich et al.	709	230	
SE	AM	5,185,861	02/1993	Valencia	711	120	
SE	AN	5,268,900	12/1993	Hluchyj et al.	370	429	
SE	AO	5,634,015	05/1997	Chang et al.	710	310	
SE	AP	5,671,446	09/1997	Rakity et al.	710	54	
SE	AQ	5,684,962	11/1997	Black et al.	709	238	
SE	AR	5,850,395	12/1998	Hauser et al.	370	398	
SE	AS	5,872,769	02/1999	Caldara et al.	370	230	
SE	AT	5,873,089	02/1999	Regache	707	100	
SE	AU	5,893,162	04/1999	Lau et al.	711	153	
SE	AV	6,320,861	11/2001	Adam et al.	370	395.7	
SE	AW	6,351,474	02/2002	Robinet et al.	370	486	
SE	AX	6,385,658	05/2002	Harter et al.	719	312	
SE	AY	6,393,531	05/2002	Novak et al.	711	154	
SE	AZ	6,438,651	08/2002	Slane	711	118	
SE	AAA	6,523,060	02/2003	Kao	709	202	

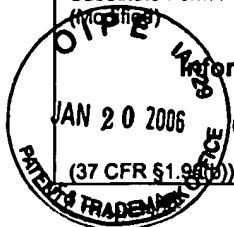
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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SE	ABB	6,539,024	03/2003	Janoska et al.	370	229	
SE	ACC	6,658,546	12/2003	Calvignac et al.	370	429	
SE	ADD	6,684,303	01/2004	LaBerge	711	154	
SE	AEE	6,687,247	02/2004	Wilford et al.	370	412	
SE	AFF	6,724,721	04/2004	Cheriton	370	429	
SE	AGG	6,728,845	04/2004	Adiletta et al.	711	154	
SE	AHH	6,731,596	05/2004	Chiang et al.	370	225	
SE	AII	6,754,223	06/2004	Lussier et al.	370	412	
SE	AJJ	6,757,791	06/2004	O'Grady et al.	370	412	
SE	AKK	6,791,989	09/2004	Steinmetz et al.	370	394	
SE	ALL	6,795,447	09/2004	Kadambi et al.	370	412	
SE	AMM	6,804,239	10/2004	Lussier et al.	370	392	
SE	ANN	6,810,426	10/2004	Mysore et al.	709	234	
SE	AOO	6,813,249	11/2004	Lauffenburger et al.	370	253	
SE	APP	6,816,498	11/2004	Viswanath	370	412	
SE	AQQ	6,822,958	11/2004	Branth et al.	370	395.31	
SE	ARR	6,822,959	11/2004	Galbi et al.	370	392	
SE	ASS	6,842,457	01/2005	Malalur	370	428	
SE	ATT	6,850,999	02/2005	Mak et al.	710	39	
SE	AUU	6,868,087	03/2005	Agarwala et al.	370	412	
SE	AVV	6,876,561	04/2005	Wolrich et al.	365	49	
SE	AWW	6,888,830	05/2005	Snyder II et al.	370	912	
SE	AXX	6,895,457	05/2005	Wolrich et al.	710	100	
SE	AYY	6,975,637	12/2005	Lenell	370	412	
	AZZ						

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	Applicant <b>Mark B. Rosenbluth et al.</b>			
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							Yes	No
SE	AAAA	0 418 447	03/1991	Europe	G06F	5/06	✓	
SE	ABBB	0 760 501	03/1997	Europe	G06F	5/06	✓	
SE	ACCC	WO 98/25210	06/1998	WIPO	G06F	15/16	✓	
SE	ADDD	WO 01/48619	07/2001	WIPO	G06F	13/00	✓	
SE	AEEE	WO 01/50247	07/2001	WIPO	G06F	9/00	✓	
SE	AFFF	WO 01/50679	07/2001	WIPO	H04L	12/00	✓	
SE	AGGG	WO 03/017541	02/2003	WIPO	H04J	1/16	✓	
	AHHH							

Other Documents (include Author, Title, Date, and Place of Publication)		
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SE	AIII	Brewer, et al., "Remote Queues: Exposing Message Queues for Optimization and Atomicity", SPAA '95 Santa Barbara, CA, pp. 1-13.
SE	AJJJ	Buyuktosunoglu, A., et al., "Tradeoffs in Power-Efficient Issue Queue Design", ISLPED '02, ACM, Aug. 2002, 6 pages.
SE	AKKK	Dandamudi, S., "Multiprocessors", IEEE Computer, Mar. 1997, pp. 82-89.
SE	ALLL	Hendler, D., et al., "Work Dealing", SPAA '02, ACM, Aug. 2002, pp. 164-172.
SE	AMMM	Jonkers, H., "Queueing Models of Shared-Memory Parallel Applications", Computer and Telecommunications Systems Performance Engineering, Pentech Press, London, 1994, 13 pages.
SE	ANNN	Kumar, S., et al., "A Scalable, Cache-Based Queue Management Subsystem for Network Processors", no date, pp. 1-7.
SE	AOOO	Lymar, T., et al., "Data Streams Organization in Query Executor for Parallel DBMS", no date, 4 pages.
SE	APPP	McLuckie, L., et al., "Using the RapidIO Messaging Unit on PowerQUICC III", Freescale Semiconductor, Inc., 2004 Rev. 1, pp. 1-19.
SE	AQQQ	Michael, M., "Scalable Lock-Free Dynamic Memory Allocation", PLDI '04, ACM, Jun. 2004, pp. 1-12.
SE	ARRR	Pan, H., et al., "Heads and Tails: A Variable-Length Instruction Format Supporting Parallel Fetch and Decode", CASES 01, No. 16-17, 2001, 8 pages.
SE	ASSS	Scott, M., "Non-Blocking Timeout in Scalable Queue-Based Spin Locks", PODC '02, ACM, Jul. 2002, pp.31-40.
	ATTT	

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